



		Subclass	ISSUE CLASSIFICATION
		Class	

**PATENT NUMBER**

**U.S. UTILITY** Patent Application

<b>O.I.P.E.</b> <i>00000000000000000000000000000000</i>	<b>PATENT DATE</b> <i>00/00/00</i>
<b>SCANNED</b> <i>00000000000000000000000000000000</i>	<b>Q.A.</b> <i>00000000000000000000000000000000</i>

π5-555

APPLICATION NO. 09/800213	CONT/PRIOR D	CLASS 257	SUBCLASS 197	ART UNIT 2815 2826	EXAMINER TRAN, MINH LOAN
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**APPLICANTS**

John Coleman

TITLE

## Semiconductor fabrication and structure for field-effect and bipolar transistor devices

PTO-2040  
12/99

**ISSUING CLASSIFICATION**

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				<b>ISSUE FEE</b>	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				Amount Due	Date Paid
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